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RESPONSE AFTER FINAL REJECTION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Kenneth Shepard

Examiner

Ayal I. Sharon

Serial No.

09/591,270

Art Unit

2123

Filed

June 9, 2000

For

METHODS FOR ESTIMATING THE BODY VOLTAGE OF

DIGITAL PARTIALLY DEPLETED SILICON-ON-

INSULATOR CIRCUITS

AMENDMENT

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450

January 31, 2005

Date of Deposit

Paul Ackerman

39,891 '

Attorney Name

PTO Registration No

Signature

January 31, 2005
Date of Signature

ignature

Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450

Dear Sir:

Applicants respectfully petition for a one-month extension of time to respond to the Office Action mailed on September 30, 2004. A check is enclosed to cover the extension fee.

Remarks begin on page 2 of this paper.